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Application Number 09/504,660 **TRANSMITTAL** Filing Date February 14, 2000 **FORM** First Named Inventor William E. Hoke **Art Unit** (to be used for all correspondence after initial filing) 2811 **Examiner Name Donghee Kang** (formerly Attorney Docket Number RTN2-047PUS Total Number of Pages in This Submission 07206/047001)

ENCLOSURES (Check all that apply)													
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				charge deposit account No. 50-0845 for the cost of such extension.									
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Date 4-22-03													
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

William E. Hoke et al.

Application No.:

09/504,660

Filed:

February 14, 2000

Entitled:

DOUBLE RECESSED

TRANSISTOR

Docket No.:

RTN2-047PUS (formerly

07206/047001)

Group Art Unit: 2811

Examiner: Donghee Kang

Certificate of Mailing (37 C.F.R. 1.8(a))

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By: Judith C. Tarrant

RESPONSE

Box AF Commissioner for Patents Washington, D.C. 20231

Sir:

This is in response to the Final Office Action mailed February 05, 2003.

REMARKS

Reconsideration and re-examination are herby requested.

With regard to claim 18, such claim points out that: (1) there is a resistive layer disposed over the Schottky; and, (2) a channel layer disposed over the substrate, the channel